Product Specification

Part Name: 1.9 inch TFT MODULE Customer Part ID: Unvision PartID: 190-1732TBWPG01-C30 Ver: A

Customer:

Approved by

From:

Approved by

Revision History

Rev.	Date	Contents	Written	Approved
А				

Special Notes

Note1.	

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1 General Description

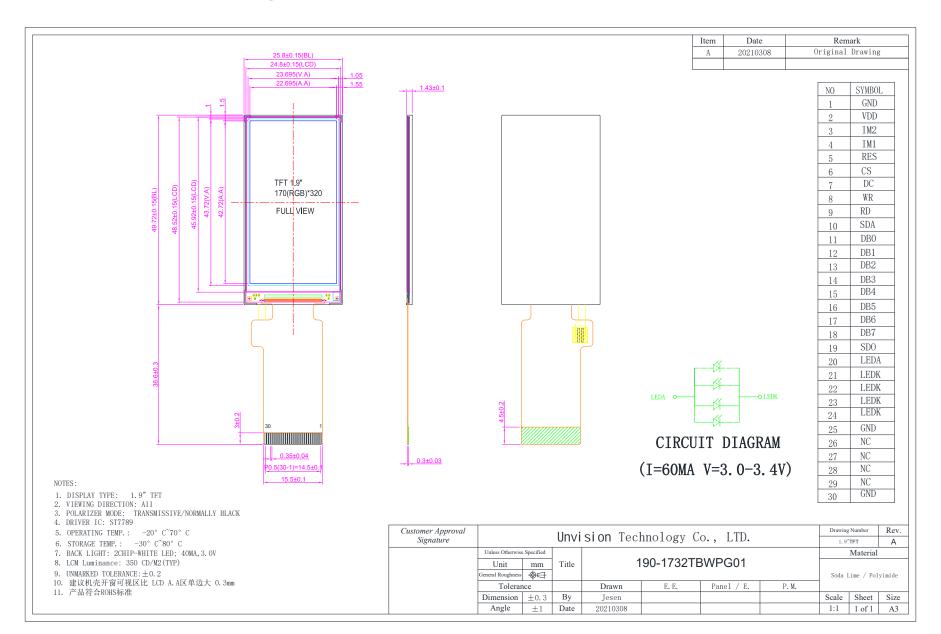
This display module is a transmissive type color active matrix TFT(Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This module is composed of a TFT LCD module, a driver circuit, and a back-light unit.The resolution of a 1.9" contains 170(RGB)X320 dots and can display up to 262k colors.

Features	Details	Unit
Display Size(Diagonal)	1.9	inch
LCD type	α-Si TFT	-
Display Mode	IPS / Transmissive / Normally Black	-
Resolution	170RGB x 320	-
View Direction	All	Best image
Module Outline	25.8(H) ×49.72(V)×1.43(T) (Note 1)	mm
TP Outline	N/A	mm
TP Viewing Area	N/A	mm
TP Active Area	N/A	mm
Active Area	22.7 (H)×42.72(V)	mm
Viewing Area	N/A	mm
Display Colors	262K	-
Interface	4-SPI/8bit 8080	-
Driver IC	ST7789V3	-
Operating Temperature	-20~60	°C
Storage Temperature	-30~70	°C
Weight	TBD	g

2 Module Parameter

Note 1: Excluding hooks, posts , FPC/FPC tail etc.

3 Mechanical Drawings



4 Module Interface

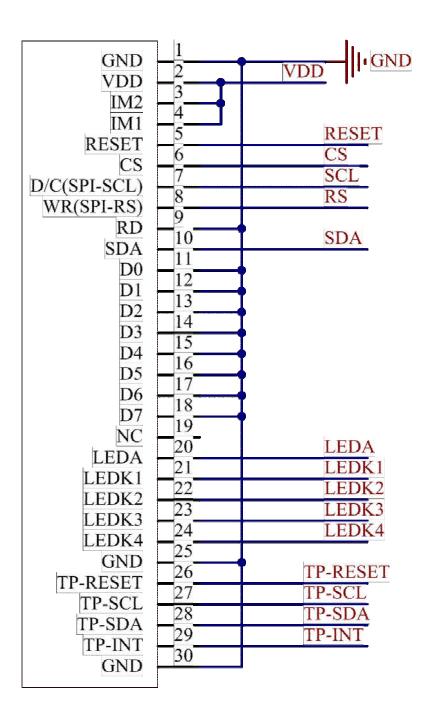
NO	SYMBOL	FUNCTION			
1	GND	Power Ground			
2	VDD	Power Supply for Analog, VDD=2.4V~3.3V.			
3	IM2	when IM1=0, IM2=0, 8080-8bit; when IM1=1, IM2=1, 4-line SPI serial I/F.			
4	IM1				
5	RESET	This signal will reset the device and it must be applied to properly initialize the chip. Signal is active low.			
6	CS	Chip selection pin; Low enable, High disable.			
7	D/C (SPI-SCL)	When connecting to an 8080-series microprocessor, this pin receives the data/command selection pin .This pin is used to be serial interface clock in 4-line serial interface.			
8	WR (SPI-RS)	When connecting to an 8080-series microprocessor, this pin receives the write signal.Display data/command selection pin in 4-line serial interface.			
9	RD	When connecting to an 8080-series microprocessor, this pin receives the Read signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial interface is selected, this pin must be connected to Ground.			
10	SDA	SPI interface input/output pin. The data is latched on the rising edge of the SCL signal.			
11-18	D0-D7	MCU parallel interface data bus.			
19	NC	No Connect			
20	LEDA	LED Anode			
21	LEDK1	LED Cathode			
22	LEDK2	LED Cathode			
23	LEDK3	LED Cathode			
24	LEDK4	LED Cathode			
25	GND	Power Ground			
26	Т	Let this pin open.			
27	NC	Let this pin open.			
28	NC	Let this pin open.			
29	NC	Let this pin open.			
30	GND	Power Ground			

5 Application Circuit

8bit 8080

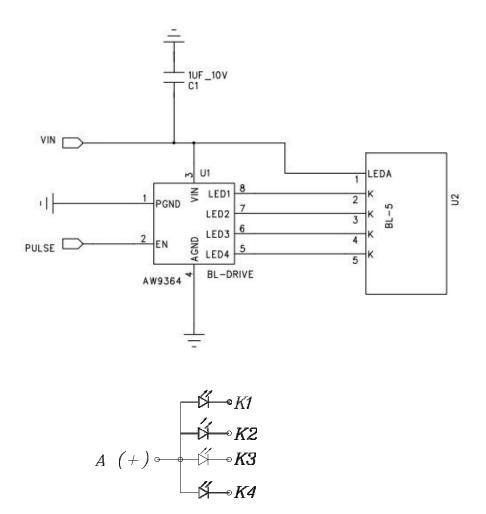
	- 10.0	
GND	1	UDD UGND
	2	VDD
VDD	3	
IM2	4	•
IM1	5	RESET
RESET	6	CS
CS	7	D/C
D/C(SPI-SCL)	8	WR
WR(SPI-RS)	9	RD
RD		KD
SDA	10	DA
D0	11	D0
D1	12	D1
D2	13	D2
D3	14	D3
D4	15	D4
D5	16	D5
D5	17	D6
D0 D7	18	D7
	19	
NC	20	LEDA
LEDA	21	LEDK1
LEDK1	22	LEDK2
LEDK2	23	LEDK3
LEDK3	24	LEDK4
LEDK4	25	
GND	26	TP-RESET
TP-RESET	27	TP-SCL
TP-SCL	28	TP-SDA
TP-SDA	29	TP-INT
TP-INT	30	11 - 11 1
GND	50	
4 <u></u>		

4-SPI



Backlight recommended circuit

Motherboard driver backlight is need constant current circuit, if the rated voltage screen after light brightness difference.Current and power consumption of the machine are inconsistent, so recommend a backlight driving circuit is best rated current.It is recommended to use IC (AW9364). The reference circuit is as follows:



Note: constant current circuit for every LED, and though LED lamp current is less than 20mA.Recommand between 15mA and 20 mA for every LED.

6 Absolute Maximum Ratings

VSS=0V, Ta=25°C

It	tem	Symbol	Min.	Max.	Unit
	Power supply	VDD	-0.3	+4.6	V
Supply Voltage	Analog	-	-	-	V
	IO	IOVDD	-0.3	+4.6	V
Input Voltage		Vi	-0.3	IOVDD+0.3	V
Storage temperature		T_{stg}	-30	+70	°C
Operating temperature		T_{op}	-20	+60	°C
Storage humidity		H _{stg}	10	Note 1	%RH
Operating humidit	У	H _{op}	10	Note 1	%RH

Note 1: 90%RH max, If Ta is below 50°C; 60%RH max, If Ta is over 60°C.

7 Electrical Specification

DC Characteristics

Item		Symbol	Min.	Typ.	Max.	Unit
	Power supply	VDD	2.4	2.8	3.3	V
Supply Voltage	Analog	VCI	2.4	2.8	3.3	V
	IO	IOVDD	1.65	1.8/2.8	3.3	V
Logic Low input voltage	V _{IL}	-0.3IOVDD	-	0.3IOVDD	V	
Logic High input volta	Logic High input voltage			-	IOVDD	V
Logic Low output volta	age	Vol	-	-	0.2IOVDD	V
Logic High output volt	age	Voh	0.8IOVDD	-	-	V
Cument Consumption	Normal display	Ivdd	-	50	-	mA
Current Consumption	Standby mode	Ivdd	-	20	-	uA
Frame Frequency		f_{FR}	-	60	-	Hz

8 AC Characteristics

Reset timing and interface timing: Please refer to IC datasheet.

9 Command Table

Please refer to IC datasheet.

10 Recommended Setting and Initialization Flow for Reference

Please refer to attached file.

11 Optical Specifications

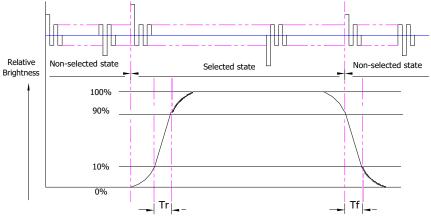
11.1 Optical Specifications

Ta=25°C, VDD=2.8V, TN LC+ Polarizer

	Item		Gkk	Condition	5	Specificatio	on	T
	Luminance on surface(I_f =20mA)		Symbol Condition		Min.	Тур.	Max.	Unit
			Lv	Normally viewing	600	650	-	cd/m ²
de)	Contrast ra	atio	CR	angle	-	600	-	-
e Mo	Response t	ime	T_R	$\theta_{X} = \theta_{Y} = 0^{\circ}$	-	10	20	
ssive			T_F	-	-	20	30	ms
imai		Red	Xr		0.614	0.644	0.674	_
Trai	Contrast range Response t Chromaticity Transmissive	Keu	Y_R		0.290	0.320	0.350	-
) uC		Chromaticity Green	X_G		0.270	0.300	0.330	-
ght (Y_G		0.540	0.570	0.600	-
ckli	Tansinissive		Xb	-	0.104	0.134	0.164	-
Ba			Y_B		0.097	0.127	0.157	-
		White	Xw		0.267	0.297	0.327	-
		white	Y_W		0.302	0.332	0.362	-
	Viewing	Horiz	θ_{X^+}		-	80	-	_
	Viewing	ontal	θx-	Center	-	80	-	Deg.
	Angle	Vertic	θ_{Y^+}	CR≥10	-	80	-	Deg.
		al	θγ-		-	80	-	
	NTSC Ratio(C	Gamut)	-	-	-	60	-	%

11.2 Definition of Response Time

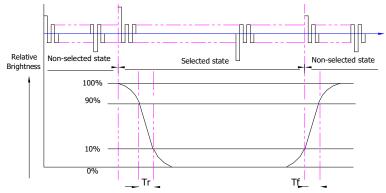




Tr is the time it takes to change form non-selected state with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

1 22 Normally White Type (Positive)



Tr is the time it takes to change form non-selected state with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

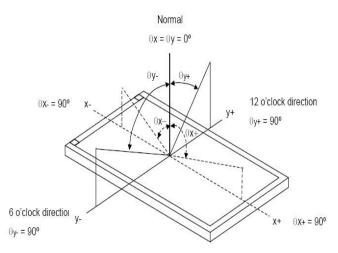
11.3 Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	BM-7 or EQUI		
Measuring Point Diameter	3mm//1mm		
Measuring Point Location	Active Area centre point		
Test nettorn	A: All Pixels white		
Test pattern	B: All Pixel black		
Contrast setting	Maximum		

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

11.4 Definition of Viewing Angles



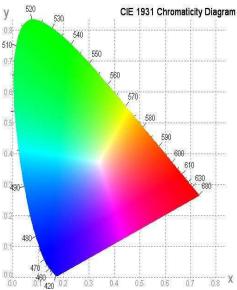
Measuring machine: LCD-5100 or EQUI

11.5 Definition of Color Appearance

R,G,B and W are defined by (x, y) on the TOPchromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)

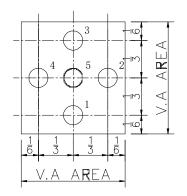


11.6 Definition of Surface Luminance, Uniformity and Transmittance

Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

- 1.6.1 Surface Luminance: LV = average(LP1:LP5)
- 1.62 Uniformity = Minimal (LP1:LP5) / Maximal (LP1:LP5) * 100%
- 1.63 Transmittance = LV on LCD / LV on Backlight * 100%

Note :Measuring machine:BM-7



12 Quality Assurance

12.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer by Iexcellence display.

12.2 Agreement Items

Iexcellence and customer shall negotiate if the following situation occurs:

- 1221 Discrepancies between Iexcellence 's QA standards and customer's QAstandards.
- 1222 Additional requirement to be added in product specification.
- 1223 Any other special problem.

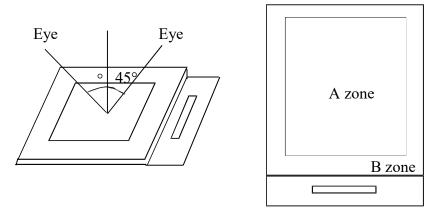
12.3 Standard of the Product Visual Inspection

123.1 Appearance inspection:

12.3.1.1 The inspection must be under illumination about 1000 - 1500 lx, and the distance of view must be at 30cm \pm 2cm.

12.3.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

12.3.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area.



1232 Basic principle: A set of sample to indicate the limit of acceptable quality level mustbe discussed by both Iexcellence and customer when there is any dispute happened.

12.4 Inspection Specification

Sampling plan according to GB/T2828.1-2012/ISO 2859-1: 1999 and ANSI/ASQC

Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.4 Minor defect: AQL 1.0

No.	Item	Criteria (Unit: mm)					
			a		Size	Area	Acc. Qty
	Black / White spot		→		φ≤0.10		Ignore
	Foreign material	ł	b		0.10<φ≤0.1	5	2
01	(Round type)				0.15<φ≤0.2	20	1
01	Pinholes Stain				0.20<φ		0
	Particles inside cell. (Minor defect)	φ=(;	a + b) /2		Total		2 no include φ≤ 0.10)
		Dista	unce between	2 defects	s should more t	han 5mm a	part.
			X		✓ ←		-
	Black and White line		L Length		Width	Acc. Qty	
	Scratch		/	W	$V \leq 0.03$	Ignore]
02	Foreign material		$L \leq 2$	0.03 <	$< W \leq 0.05$	1	
° -	(Line type)		/	0.0)5 < W	0	
		I		Total		1	

(Mino	or defect)	
	Distance be	etween 2 defects should more than 5mm apart.
	Scratches r	ot viewable through the back of the display are
	acceptable.	

	Item	Criteria (Unit: mm)				
03	Glass Crack (Minor defect)	LCD with extensible crack line is unacceptable(When press the cracked LCD area, the line will expand, we define it is extensible crack line)				
	Glass Chipping Pad Area: (Minor defect)		gnore			
04	Area: (Millior defect)					
		$\begin{array}{c c} \hline \\ Length and Width \\ \hline \\ c > 3.0, b < 1.0 \\ \hline \end{array}$	cc. Qty			
05	Glass Chipping Rear of Pad Area: (Minor defect)	c< 3.0, b< 1.0 c< 3.0, b< 1.0 c< 3.0, b< 0.5 a <glass td="" thickness<=""><td>2 4</td></glass>	2 4			
	0 3 3 3 4 C C C					
			cc. Qty			
	Glass Chipping Except Pad Area: (Minor defect)	a <glass td="" thickness<=""><td></td></glass>				
06						

No.	Item		Length rendr Widthi	t: man) Qty			
	Glass Corner		c < 2.0, b< 1.5	Ignore			
	Chipping:		c < 1.5, b< 2	Ignore			
	(Minor defect)		a <glass td="" thic<=""><td>kness</td><td></td></glass>	kness			
07					1		
	°â y∕ ∓ ≪c						
	Glass Burr:						
	(Minor defect)	Glass burr do	't affect assemble and	m Acq. Otivaar	sion		
			F < 0.5	Ignore	Sion.		
0.0				0	l		
08							
	-						
	FPC Defect:						
	(Minor.defect)	9.1 Dent, pinhole width $a < w/2$.					
00	a	(w: circuitry w					
09	W		0.2 Open circuit is unacceptable.				
		9.3 No oxidation, contamination and distortion.					
	a						
		Test for inserti	on of plug gauge at hi	ghest warping	point:		
	Screen deformation	(0.96-3.1 inches does not contain 3.1)					
10	· · ·	H≦0.25MM					
		The client has	special requirements,a	according to dr	rawing		
					8		
			Diameter	Acc. Qty			
	Bubble on Polarizer (Minor defect)		φ≤0.15	Ignore			
11			0.15 <φ≤0.20	2			
			0.20 <φ≤0.30	1			
			0.3 < φ	None			

No.	Item	Criteria (Unit: mm)			
	Dent on Polarizer (Minor defect)		Diameter	Acc. Qty	
12			φ≤0.15	Ignore	
			0.15 <φ≤0.20	2	
			0.20 <φ≤0.30	1	
			$0.3 < \varphi$	None	
13	Bezel	13.1 No rust, distortion on the Bezel.13.2 No visible fingerprints, stains or other contamination.			
14	Touch Panel	 D: Diameter W: width L: length 14.1 Spot: D≤0.20 is acceptable 0.20<d≤0.3, 3<="" acceptable="" li="" qty,=""> 2dots are acceptable and the distance between defects should mor e than 5mm. D>0.3 is unacceptable 14.2 Dent: D>0.30 is unacceptable 14.3 Scratch: W≤0.03, L≤10 is acceptable, 0.03<w≤0.10, ,acceptable="" 3<="" li="" l≤10="" qty,=""> Distance between 2 defects should more than 5 mm. W>0.10 is unacceptable. </w≤0.10,></d≤0.3,>			
15	РСВ	 15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F. 			
16	Soldering	Follow IPC-A-610C standard			

No.	Item	Criteria (Unit: mm)		
17	Electrical Defect (Major defect)	 The below defects must be rejected. 17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function. 17.9 Dark Dot – one Allowed. 17.10 Bright Dot – one Allowed. Remark: 1. A pixel defect is acceptable if one color is none functionaland causes a bright dot. The display may have one case where one color is out and cause a dark dot. 2. Bright dot caused by scratch and foreign object accords to item1. 		
18	Leak	Yellow light,OK; White light,According to the limit sample		

Remark: Visual and cosmetic defects are rejectable only if these fall within the LCD viewing area.

12.5 Classification of Defects

Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

12.6 Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

12.7 Packing

127.1 There should be no damage of the outside carton box, each packaging box should has label in the correct location per packing drawing requirement.

1272 All direct package materials shall offer ESD protection.

13 Reliability Specification

Item	Condition	Cycle Time	Quantity	Remark
Constant Temp. and Constant Humidity Operation Test	$+40 \pm 3^{\circ}C,90 \pm 3\%RH$	96hrs		
High Temp. Operation Test	$+70 \pm 3^{\circ}C$	96hrs		*1

Low Temp. Operation Test	$-20 \pm 3^{\circ}C$	96hrs		
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Thermal Shock Test	-20 ± 3°C (30min) +70 ± 3°C (30min)	10cycles		
ESD Test(end product)	150pF, 330Ω, ±2KV, Contact	10times		*2, *3
Vibration Test (for packaging)	150pF, 330Ω , $\pm 6KV$, Air Frequency: 10Hz to 55Hz to10Hz,Swing:1.5mm,time: X,Y,Z each 2H.	6hrs	One inner carton	*4

Note 1. For humidity test, DI water should be used.

Inspection Standard: Inspect after 1-2hrs storage at room temperature, the sample shall be free from the following defects:

- Air bubble in the LCD
- Seal Leakage
- Non-display
- Missing Segment
- Glass Crack
- IDD is greater than twice initial value.
- Others as per QA Inspection Criteria
- Note 2. No defect is allowed after testing

The End Product ESD value is only indicative and depends on customer ESD protection design for the whole system.

Note 3. ESD should be applied to LCD glass panel, not other areas (such as on IC and so on) IDD should be within twice initial value.

In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

Note 4. Only upon request.

14 Precautions and Warranty

14.1 Safety

141.1 The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap andwater.

1412 Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

14.2 Handling

1421 Reverse and use within ratings in order to keep performance and prevent damage.

1422 Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the

LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

14.3 Operation

143.1 Do not drive LCD with DC voltage

1432 Response time will increase below lower temperature

1433 Display may change color with different temperature

1434 Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear "fractured".

14.4 Static Electricity

1441 CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

1442 The normal static prevention measures should be observed for work clothes and benches.

1443 The module should be kept into anti-static bags or other containers resistant to static for storage.

14.5 Limited Warranty

145.1 Unless otherwise agreed between TOP-DISPLAY and customer, TOP-DISPLAY will replace or repair any of its LCD and LCM whichTOP-DISPLAY found to be defectiveelectrically and visually when inspected in accordance withTOP-DISPLAY Quality Standards, for a period of one year from date of shipment.

1452 The warranty liability of TOP-DISPLAY is limited to repair and/or replacement. TOP-DISPLAY will not be responsible for any consequential loss.

1453 If possible, we suggest you use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.

15 Packaging

TBD

16 Prior Consult Matter

1. ForTOP-DISPLAY standard products, we keep the right to change material, process for improving the product property without prior notice to our customer.

2. For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.

3. If you have special requirement about reliability condition, please let us know before you start the test on our samples.