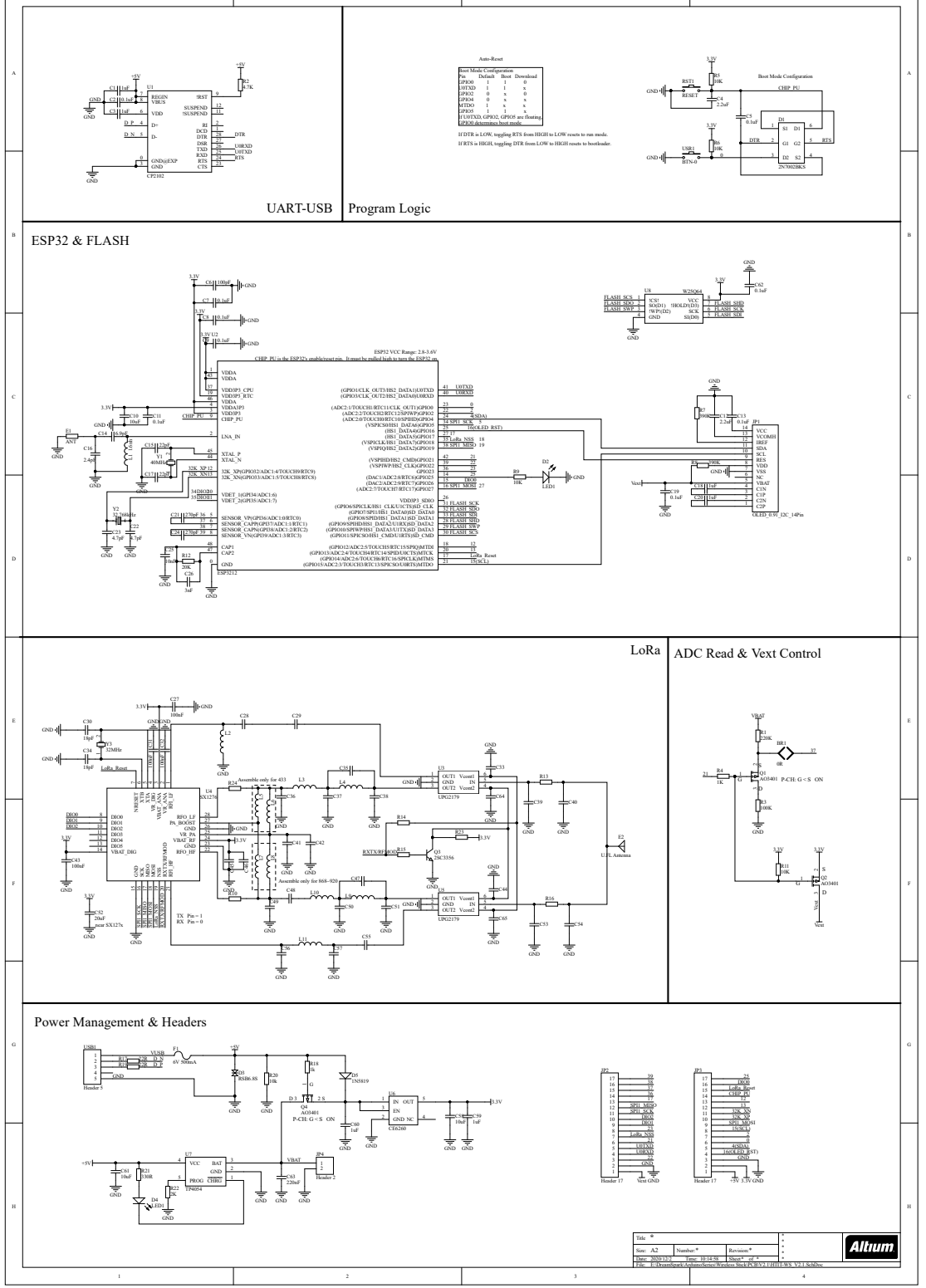
UART-USB
Program Logic
Auto-Reset
Boot Mode Configuration
Pin Default Boot Download
GPIO0 1 1 0
U0TXD 1 1 x
GPIO2 0 x 0
GPIO4 0 x x
MTDO 1 x x
GPIO5 1 1 x
If U0TXD, GPIO2, GPIO5 are floating, GPIO0 determines boot mode.
If DTR is LOW, toggling RTS from HIGH to LOW resets to run mode.
If RTS is HIGH, toggling DTR from LOW to HIGH resets to bootloader.
Boot Mode Configuration
CHIP_PU
2N7002BKS
ESP32 & FLASH
ESP32 VCC Range: 2.8-3.6V
CHIP_PU is the ESP32's enable/reset pin. It must be pulled high to turn the ESP32 on.
ESP32
W25Q64
OLED_0.91_I2C_14Pin
LoRa
ADC Read & Vext Control
Power Management & Headers
Header 5
Header 2
Header 17
Header 17
Title *
Size: A2
Number: *
Revision: *
Date: 2020/12/2
Time: 10:14:56
Sheet * of *
File: E:\Dynamic\Arduino\Sensor\Wireless Stick\PCB\V3.1\HTTU\WS_V2.1.SchDoc
Altium